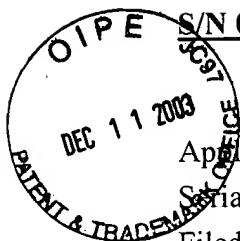


EXPEDITED PROCEDURE – EXAMINING GROUP 2822



S/N 09/256643

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Leonard Forbes et al.

Examiner: Michael Trinh

Serial No.: 09/256643

Group Art Unit: 2822

Filed: February 23, 1999

Docket No.: 303.324US2

Title: TRANSISTOR WITH VARIABLE ELECTRON AFFINITY GATE AND METHODS
OF FABRICATION AND USE

AMENDMENT & RESPONSE UNDER 37 C.F.R. 1.116

Mail Stop RCE
Commissioner for Patents
P.O.Box 1450
Alexandria, VA 22313-1450

In response to the final Office Action dated 6 August 2003, the applicant requests
reconsideration of the above-identified application in view of the following.

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